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USB HOST SYSTEM-

Abstract of the Disclosure

This invention is a USB host comprising a first processor implementing a function of a USB system and presenting a high-level pipe view of USB to a second processor. In one embodiment of this invention, first processor and the second processor communicate through a data communication memory (DCM) which can be accessed by both the first processor and the second processor, using a plural of predefined transfer record formats. From the second processor's point of view, a USB transfer becomes reading from and writing to the DCM with predefined record formats which can be done by filling templates.